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# Ordinal machines and admissible recursion theory

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#### 1. Introduction

#### ABSTRACT

We generalize standard Turing machines, which work in time  $\omega$  on a tape of length  $\omega$ , to  $\alpha$ -machines with time  $\alpha$  and tape length  $\alpha$ , for  $\alpha$  some limit ordinal. We show that this provides a simple machine model adequate for classical admissible recursion theory as developed by G. Sacks and his school. For  $\alpha$  an admissible ordinal, the basic notions of  $\alpha$ -recursive or  $\alpha$ -recursively enumerable are equivalent to being computable or computably enumerable by an  $\alpha$ -machine, respectively. We emphasize the algorithmic approach to admissible recursion theory by indicating how the proof of the Sacks–Simpson theorem, i.e., the solution of Post's problem in  $\alpha$ -recursion theory, could be based on  $\alpha$ -machines, without involving constructibility theory.

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Ideas and methods of classical recursion theory have been lifted from the natural numbers to other kinds of mathematical objects. Takeuti [14], Kreisel and Sacks [6], Kripke [7], and Platek [10] defined recursion theory on *ordinals*, making use of the arithmetic and order-theoretic similarities between natural numbers and ordinal numbers. This work led to the concept of *admissible* ordinals and to  $\alpha$ -recursion theory where recursions are carried out on (the elements of) an admissible ordinal  $\alpha$ . The field of  $\alpha$ -recursion theory was developed comprehensively by G. Sacks and his school since 1965. Sacks gave the following characterization in his definitive monograph [11, p. 149]:

 $\alpha$ -recursion theory lifts classical recursion theory from  $\omega$  to an arbitrary  $\Sigma_1$  admissible ordinal  $\alpha$ . Many of the classical results lift to every  $\alpha$  by means of recursive approximations and fine structure techniques.

The lifting is based on the observation that a set  $A \subseteq \omega$  is recursively enumerable iff it is definable by a  $\Sigma_1$ -formula over  $(H_{\omega}, \in)$ , the set of all hereditarily finite sets. By analogy, a set  $A \subseteq \alpha$  is said to be  $\alpha$ -recursively enumerable iff it is  $\Sigma_1(L_{\alpha})$ , i.e., definable by a  $\Sigma_1$ -formula, allowing parameters, over  $(L_{\alpha}, \in)$  where  $L_{\alpha}$  is the  $\alpha$ th level of Gödel's constructible hierarchy. Consequently a set  $A \subseteq \alpha$  is said to be  $\alpha$ -recursive iff it is  $\Delta_1(L_{\alpha})$ , i.e., if the set and its complement are  $\alpha$ -recursively enumerable. So  $\alpha$ -recursion theory is closely connected to set theory, in particular to constructibility theory. Its methods involve set-theoretic definability arguments up to the beginnings of Jensen's fine structure theory of the constructible hierarchy. Further information on the connection to set theory can be found in [1].

Besides definability and constructibility techniques, there has always been a strong "computational" attitude in  $\alpha$ -recursion theory as described by Sacks [11, p. 155] in the discussion of a  $\Sigma_1(L_{\alpha})$ -definition (of some function f):

The definition of f can be thought of as a *process*. At *stage*  $\delta$  it is assumed that all *activity* at previous stages is encapsulated in an  $\alpha$ -finite object,  $s \upharpoonright \delta$ . In general it will be necessary to *search* through  $L_{\alpha}$  for some existential witness ... [emphases by the present authors].

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However, no machine model for admissible recursion theory was elaborated. Levy announced a generalization of Turing machines working on regular cardinals in the abstract [8], but no further details were published. Other approaches to ordinal recursion theory were based on recursion schemata (see Takeuti [14], Machover [9]).

In this article we show that  $\alpha$ -recursion does indeed correspond naturally to computations by (abstract) machines: recursive enumerability and recursiveness in admissible recursion theory is equivalent to enumerability and computability by certain Turing *machines* working on ordinals. This was proved by the first author (Section 2) after a talk by Sy Friedman on ordinal recursion theory at the Bonn International Workshop on Ordinal Computability (BIWOC) in January 2007. The second author recast the proof of the Sacks–Simpson theorem using the computational paradigm instead of constructibility theory. The crucial point involved was how the informally presented recursions in the argument of Sacks and Simpson [11, 12] (and a recursion method presented by Shore [13]) can be implemented by means of computational mechanisms of the generalized Turing machines (see Section 4). We are very thankful to Russell Miller for his generous help with the discussion of the priority arguments.

Ordinal computability is introduced here on the basis of a Turing machine model, but the theory is robust with respect to various modifications. One could, e.g., use Turing machines with any finite number of tapes and read–write heads, change the commands of the machine, or work with register machines instead of Turing machines.

#### 2. Ordinal Turing machines and the constructible hierarchy

A standard Turing machine is based on the set  $\omega = \{0, 1, ...\}$  of natural numbers: it acts on a Turing tape of length  $\omega$  within a discrete time axis which is also indexed by  $\omega$ . In [5], the first author defined *ordinal* Turing *machines* by replacing the set  $\omega$  of natural numbers by the class Ord of ordinal numbers. In this article we generalize both standard and ordinal Turing machines to  $\alpha$ -Turing *machines*, or  $\alpha$ -machines for short, where space and time are indexed by (the elements of) some fixed limit ordinal  $\alpha$  or by  $\alpha = \infty =$  Ord. We define  $\alpha$ -machines by an intuitive description of  $\alpha$ -computations. The relationship between ordinal Turing machines and the constructible model *L* was studied in [5]. We shall make use of those methods by restricting them to  $\alpha$ .

An  $\alpha$ -machine possesses a *tape* of length  $\alpha$  which consists of *cells* containing the symbols 0 or 1 where 0 is the default symbol. So at every *time*  $t < \alpha$  the tape can be formalized by a 0–1-sequence

$$T(t): \alpha \rightarrow 2, \qquad T(t) = (T_0(t), T_1(t), \ldots).$$

A read–write head is moving on the tape, positioned at an ordinal  $H(t) < \alpha$  at time  $t < \alpha$ . The head starts at cell 0, i.e., H(0) = 0. The computation is steered by a (standard) Turing program which is a finite set *P* of numbered commands of the two forms

s: if head=c then print c', move right, and change to state s'

or

s: if head=c then print c', move left, and change to state s'

where c, c'  $\in \{0, 1\}$  and s, s'  $\in \omega$ . At time t the machine is in some state  $S(t) \in \omega$ , starting from state 0, i.e., S(0) = 0. A computation of the machine is a sequence

 $(T(t), H(t), S(t))_{t < \theta}$ 

of machine configurations (T(t), H(t), S(t)) for t below some maximal  $\theta \le \alpha$  at which the computation *stops*. The computation is defined by recursion on  $t < \theta$ . The *initial configuration* is of the form (T(0), 0, 0).

At successor times t + 1 the configuration is defined from the configuration (T(t), H(t), S(t)) as follows. Let s = S(t), and let  $c = T_{H(t)}(t)$  be the symbol under the machine's head.

*Case 1*. The program contains a command of the form

s: if head=c then print c', move right, and change to state s'

Then the machine acts accordingly by setting

$$T_{\xi}(t+1) = \begin{cases} T_{\xi}(t), \text{ if } \xi < \alpha \text{ and } \xi \neq H(t) \\ c', \text{ otherwise} \end{cases}$$
$$H(t+1) = H(t) + 1$$
$$S(t+1) = s'$$

and proceeds to time  $t + 1 < \alpha$ . *Case 2*. Not *Case 1* and the program contains a command of the form

s: if head=c then print c', move left, and change to state s'

Then set

$$T_{\xi}(t+1) = \begin{cases} T_{\xi}(t), \text{ if } \xi < \alpha \text{ and } \xi \neq H(t) \\ \text{c', otherwise} \end{cases}$$
$$H(t+1) = \begin{cases} H(t) - 1, \text{ if } H(t) \text{ is a successor ordinal} \\ 0, \text{ otherwise} \end{cases}$$
$$S(t+1) = \text{s'}$$

and proceed to time  $t + 1 < \alpha$ . Note that if H(t) is a limit ordinal then the head position is reset to 0.

*Case 3.* Not *Case 1* and not *Case 2*. Then the computation stops, i.e.,  $\theta$  is set to the successor ordinal t + 1.

The configuration at *limit times*  $t < \alpha$  is obtained as a natural limit of previous configurations, using lim inf-operations:

$$T(t)_{\xi} = \liminf_{s \to t} T(s)_{\xi}$$
  

$$S(t) = \liminf_{s \to t} S(s)$$
  

$$H(t) = \liminf_{s \to t, S(s) = S(t)} H(s).$$

So, if the contents  $T(s)_{\xi}$  of the  $\xi$  th cell of the tape stabilize before time t then at time t the  $\xi$ th cell contains that stable value; otherwise  $T(t)_{\xi}$  is set to the default 0. The definitions of S(t) and H(t) can be motivated as follows. Since a Turing program is finite, its execution will lead to some (complex) looping structure with loops, subloops and so forth which can be presented by pseudo-code like:

Assume that for times  $s \to t$  the loop (17 - 32) with its subloop (21 - 29) is traversed cofinally often. At limit time t it is natural to put the machine back to the beginning (17) of the "main loop". Assuming that the lines of the program are enumerated in increasing order, this corresponds to the lim inf rule  $S(t) = \liminf_{s \to t} S(s)$ . The natural head location H(t) is then determined as the inferior limit of all those head locations when the program is at the start of the "main loop".

A computation of the  $\alpha$ -machine may be visualized by a "space–time" diagram like:

				$\mathbf{S}$	р	a	с	е		$\alpha$				
		0	1	2	3	4	5	6	7		 ω	 $\theta < \alpha$		
	0	1	1	0	1	0	0	1	1		 1	 0	0	
	1	0	1	0	1	0	0	1	1		1			
T	2	0	0	0	1	0	0	1	1		1			
i	3	0	0	0	1	0	0	1	1		1			
m	4	0	0	0	0	0	0	1	1		1			
е	:													
	n	1	1	1	1	0	1	0	1		1			
$\alpha$	n+1	1	1	1	1	1	1	0	1		1			
	÷	:	:	:	:	:								
	ω	0	0	1	0	0	0	1	1		 1			
	$\omega + 1$	0	0	1	0	0	0	1	1		0			
	:		·											
	$\theta - 1$	1	0	0	1	1	1	1	0		 	 0		
	$\theta < \alpha$	S	Т	0	Р									

A computation of an  $\alpha$ -machine, head positions are indicated by shading.

The computation

 $(T(t),H(t),S(t))_{t<\theta}$ 

defined above is called the  $\alpha$ -computation by *P* with input *T*(0). If the  $\alpha$ -computation stops at some  $\theta < \alpha$  then  $\theta = t_0 + 1$  is a successor ordinal and *T*( $t_0$ ) is the final tape content. In this case we say that *P* computes *T*( $t_0$ ) from *T*(0) and write

 $P: T(0) \mapsto T(t_0)$ . Note that at all times *t* the head position H(t) is  $\leq t$  and that  $T(t) = (T(t) \upharpoonright t) \cup (T(0) \upharpoonright [t, \alpha))$ . So the  $\alpha$ -computation can be described by T(0) and the sequence

$$(T(t) \upharpoonright t, H(t), S(t))_{t < \theta}$$
.

We can define various notions of *computability* from this machine model. Information is entered and output on the one working tape of the machine. Instead of introducing several machine tapes we separate various kinds of information on the tape by dividing it up into four "subtapes", using ordinal arithmetic modulo 4: ordinals  $\equiv 0 \pmod{4}$  are used to code the "input"  $X \subseteq \alpha$ , ordinals  $\equiv 1 \pmod{4}$  code the "output"  $Y \subseteq \alpha$ , ordinals  $\equiv 2 \pmod{4}$  code extra "parameters"  $p \subseteq \alpha$ , and ordinals  $\equiv 3 \pmod{4}$  may contain an "oracle"  $0 \subseteq \alpha$ . Appropriate codings and decodings are given by the characteristic function  $X|Y|p|0: \alpha \rightarrow 2$ , where for  $\xi < \alpha$  and i < 4

$$4 \cdot \xi + i \mapsto 1 \text{ iff } (i = 0 \land \xi \in X) \lor (i = 1 \land \xi \in Y) \lor (i = 2 \land \xi \in p) \lor (i = 3 \land \xi \in O).$$

We can use an  $\alpha$ -machine to (effectively) enumerate all the pairs (*P*, *p*) where *P* is a Turing program and  $p \subseteq \alpha$  a finite set of ordinal parameters. Fix such an enumeration.

**Definition 1.** Let  $\epsilon < \alpha$  and denote by  $\{\epsilon\}$  the  $\epsilon$ th pair (P, p), consisting of a program P and parameters p. Let  $\delta \in \alpha, B \subseteq \alpha$  and  $\sigma < \alpha$ . Consider  $(T(t), H(t), S(t))_{t < \theta}$ , the  $\alpha$ -computation by P on input  $T(0) = \{\delta\} |\emptyset| p|B$ .

- $\{\epsilon\}^{B}_{\sigma}(\delta) \downarrow$  means that the computation halts in at most  $\sigma$ -many stages, i.e.  $\theta \leq \sigma$ .
- $\{\epsilon\}^{\bar{B}}(\delta) \downarrow$  means that the computation halts, i.e.  $\theta < \alpha$ .
- $\{\epsilon\}^{B}(\delta) \uparrow$  means that the computation *diverges*, i.e.  $\theta = \alpha$ .
- We write  $\{\epsilon\}_{\sigma}^{B}(\delta) \downarrow = \gamma$  iff  $\{\epsilon\}_{\sigma}^{B}(\delta) \downarrow$  and  $P : \{\delta\} |\emptyset| p | B \mapsto X' | \{\gamma\} | p | B$  for some  $X' \subseteq \alpha$  and furthermore for all  $t < \sigma \cap \theta$ T(t) is of the form  $X_t | Y_t | p | B$  for some  $X_t, Y_t \subseteq \alpha$ .

If  $B = \emptyset$  we write  $\{\epsilon\}$  instead of  $\{\epsilon\}^B$ .

**Definition 2.** A partial function  $F : \alpha \rightarrow \alpha$  is  $\alpha$ -computable in (the oracle)  $B \subseteq \alpha$  iff there is an  $\epsilon < \alpha$  such that for all  $\delta < \alpha$ :

- $\{\epsilon\}^{B}(\delta) \downarrow \text{iff } \delta \in \text{dom}(F)$
- $\{\epsilon\}^{B}(\delta) \downarrow = F(\delta) \text{ for every } \delta \in \operatorname{dom}(F).$

In that case we say that  $\{\epsilon\}^B$  computes *F* and write  $\{\epsilon\}^B = F$ .

A set  $A \subseteq \alpha$  is  $\alpha$ -computable in (the oracle) B iff its characteristic function  $\chi_A : \alpha \to 2$  is  $\alpha$ -computable in B. A set  $A \subseteq \alpha$  is  $\alpha$ -computably enumerable in (the oracle) B iff A = dom(F) for some partial function  $F : \alpha \to 2$  which is  $\alpha$ -computable in B. In case  $B = \emptyset$  we simply write  $\alpha$ -computable and  $\alpha$ -computably enumerable.

We also write *ordinal machine*, *ordinal computable*, and *ordinal computably enumerable* instead of  $\infty$ -machine,  $\infty$ -computable, and  $\infty$ -computable, resp.

These are the basic notions of  $\alpha$ -computability theory, defined in close analogy to the notions of classical computability or recursion theory. To study the relation of  $\alpha$ -computability theory to  $\alpha$ -recursion theory we link  $\alpha$ -computability to constructibility theory. Since an  $\alpha$ -computation is defined by very simple recursion rules it can be carried out within the levels  $L_{\delta}[\cdot]$  of appropriate relativized constructible hierarchies.

**Lemma 3.** Let *P* be a program and assume that the initial tape content is the characteristic function of a set  $D \subseteq \alpha$ :  $T(0) = \chi_D$ . Let  $(T(t), H(t), S(t))_{t < \theta}$  be the  $\alpha$ -computation by *P* with input T(0). Then:

(a) If  $\delta$  is a limit ordinal then  $\forall \nu < \delta (T(t) \upharpoonright t, H(t), S(t))_{t \in \theta \cap \nu} \in L_{\delta}[D].$ 

- (b) If  $\delta$  is a limit ordinal then  $(T(t) \upharpoonright t, H(t), S(t))_{t \in \theta \cap \delta}$  is uniformly  $\Delta_1(L_{\delta}[D])$ .
- (c) If  $A \subseteq \alpha$  is  $\alpha$ -computably enumerable in the oracle B then it is  $\Sigma_1(L_{\alpha}[B])$ .
- (d) If  $A \subseteq \alpha$  is  $\alpha$ -computable in *B* then it is  $\Delta_1(L_{\alpha}[B])$ .
- (e) If  $A \subseteq \alpha$  is  $\alpha$ -computably enumerable then it is  $\Sigma_1(L_{\alpha})$ .
- (f) If  $A \subseteq \alpha$  is  $\alpha$ -computable then it is  $\Delta_1(L_\alpha)$ .

**Proof.** We prove (a) and (b) by simultaneous induction on  $\delta$ . (a) holds readily for  $\delta = \omega$  since  $L_{\omega}[D] = H_{\omega}$  is the set of all hereditarily finite sets.

We assume the (a) holds at  $\delta$  and show that (b) holds at  $\delta$ . As remarked above,

 $(T(t) \upharpoonright t, H(t), S(t))_{t \in \theta \cap \delta}$ 

is basically the  $\alpha$ -computation by P with input T(0), restricted to  $\theta \cap \delta$ . It is defined by the recursive computation rules by the program P with input T(0). The recursive rules can be defined by  $\Sigma_0$ -formulas, and so by the recursion theorem for ordinal recursion  $(T(t) \upharpoonright t, H(t), S(t))_{t \in \theta \cap \delta}$  is  $\Delta_1$ -definable in the set-theoretic universe. The unbounded quantifiers in the  $\Delta_1$ -representation range over initial segments of the recursive functions. By (a), these initial segments are elements of  $L_{\delta}[D]$ and so  $(T(t) \upharpoonright t, H(t), S(t))_{t \in \theta \cap \delta}$  is  $\Delta_1(L_{\delta}[D])$ .

Now assume that  $\delta$  is a limit ordinal such that (a) and (b) hold for all limit ordinals  $\delta' < \delta$ . We show that (a) holds at  $\delta$ . This is obvious if  $\delta$  is a limit of limit ordinals. Assume now that  $\delta = \delta' + \omega$ , where  $\delta'$  is a limit ordinal. If  $\theta < \delta'$ , (a) holds trivially at

 $\delta$ . So assume  $\theta \geq \delta'$ . Then  $(T(t) \upharpoonright t, H(t), S(t))_{t < \delta'}$  is uniformly  $\Delta_1(L_{\delta'}[D])$ . The limit configuration  $(T(\delta') \upharpoonright t, H(\delta'), S(\delta'))$  is definable by simple lim inf rules from  $(T(t) \upharpoonright t, H(t), S(t))_{t < \delta'}$ . So it is also definable over  $L_{\delta'}[D]$  and

 $(T(t) \upharpoonright t, H(t), S(t))_{t \le \delta'} \in L_{\delta'+1}[D].$ 

For  $\nu \in [\delta', \delta)$ , the sequences  $(T(t) \upharpoonright t, H(t), S(t))_{t \in \theta \cap \nu}$  are "finite variations" of  $(T(t) \upharpoonright t, H(t), S(t))_{t \leq \delta'}$  and hence  $(T(t) \upharpoonright t, H(t), S(t))_{t \in \theta \cap \nu} \in L_{\delta' + \omega}[D]$ , as required by (b).

Now (b) implies (c), (c) implies (d), (c) implies (e), and (d) implies (f).  $\Box$ 

To prove the converse of (c) and (d), we represent the constructible hierarchy  $L_{\delta}[D]$ , for  $D \subseteq$  Ord by an ordinal program. According to [5], basic functions like the Gödel-pairing function are ordinal computable. This allows to code sequences of ordinals and formulas as ordinals; elementary operations on sequences can be assumed to be ordinal computable.

To make  $L_{\alpha}[D]$  accessible to an  $\alpha$ -Turing machine introduce a language with symbols (, ), {, }, |,  $\in$ , =,  $\wedge$ ,  $\neg$ ,  $\forall$ ,  $\exists$ ,  $\dot{B}$  and variables  $v_0, v_1, \ldots$  Define (*bounded*) formulas and (*bounded*) terms by a common recursion on the lengths of words formed from these symbols:

- the variables  $v_0, v_1, \ldots$  are terms;

- if *s* and *t* are terms then  $\dot{B}(s)$ , s = t and  $s \in t$  are formulas;

- if  $\varphi$  and  $\psi$  are formulas then  $\neg \varphi$ ,  $(\varphi \land \psi)$ ,  $\forall v_i \in v_j \varphi$  and  $\exists v_i \in v_j \varphi$  are formulas;

- if  $\varphi$  is a formula then  $\{v_i \in v_j | \varphi\}$  is a term.

For technical reasons we use tidy terms and formulas in which

- no bound variable occurs free,

- every free variable occurs exactly once.

An *assignment* is a finite sequence  $a : k \to V$ ; a(i) will be the *interpretation* of the variable  $v_i$ . We write t[a] and  $\varphi[a]$  for the values of t and  $\varphi$  under the assignment a. Concerning the constructible hierarchy  $L_{\delta}[D]$ , it can be shown by induction on  $\delta$  that every element of  $L_{\delta}[D]$  is the interpretation  $t[(L_{\gamma_0}[D], \ldots, L_{\gamma_{k-1}}[D])]$  of some *tidy* term t with an assignment  $(L_{\gamma_0}[D], \ldots, L_{\gamma_{k-1}}[D])$  where  $\gamma_0, \ldots, \gamma_{k-1} < \delta$ .

We define the (bounded) truth function  $W_D : A \rightarrow 2$  for L[D] on the class

 $A = \{(a, \varphi) | a \in \text{Ord}^{<\omega}, \varphi \text{ is a tidy bounded formula}\};$ 

$$W_D((\gamma_0,\ldots,\gamma_{k-1}),\varphi)=1 \text{ iff } \varphi[(L_{\gamma_0}[D],\ldots,L_{\gamma_{k-1}}[D])].$$

Relativizing the main technical result of [5] to the oracle D yields:

**Lemma 4.** The bounded truth function  $W_D$  for L[D] is ordinal computable in the oracle D by some Turing program  $P_{\text{truth}}$ .

A close inspection of the program  $P_{\text{truth}}$  shows that the computation takes place in *exponential time*, i.e., there is an exponential expression  $p(\xi)$  such that for  $((\gamma_0, \ldots, \gamma_{k-1}), \varphi) \in A$  the computation of the truth value  $W((\gamma_0, \ldots, \gamma_{k-1}), \varphi)$  stops before stage  $p(\max(\gamma_0, \ldots, \gamma_{k-1}))$ , where the exponential expression is evaluated in ordinal arithmetic. This proves:

**Lemma 5.** If the ordinal  $\alpha > 0$  is closed with respect to ordinal exponentiation then the bounded truth function  $W_D \upharpoonright (A \cap (\alpha^{<\omega} \times V))$  is  $\alpha$ -computable in the oracle  $D \cap \alpha$ .

**Lemma 6.** Let  $\alpha > 0$  be closed with respect to ordinal exponentiation and let  $A \subseteq \alpha$  be  $\Sigma_1(L_{\alpha}[B])$ . Then A is  $\alpha$ -computably enumerable in the oracle B. If  $A \subseteq \alpha$  is  $\Delta_1(L_{\alpha}[B])$  then A is  $\alpha$ -computable in the oracle B.

**Proof.** Consider a  $\Sigma_1(L_{\alpha}[B])$ -definition of  $A \subseteq \alpha$ :

$$\xi \in A \leftrightarrow \exists y \in L_{\alpha}[B] \ L_{\alpha}[B] \models \varphi[\xi, y, \vec{a}]$$

where  $\varphi$  is a bounded formulas. This is equivalent to

 $\xi \in A \leftrightarrow \exists \beta < \alpha L_{\beta}[B] \models \exists y \varphi[\xi, y, \vec{a}]$ 

and

 $\xi \in A \leftrightarrow \exists \beta < \alpha \ W_B((\xi, \beta, \vec{a}), \varphi^*) = 1$ 

where  $\varphi^*$  is an appropriate tidy formula.

Now  $\xi \in A$  is  $\alpha$ -computably enumerable in *B* by the following "search procedure": for  $\gamma < \alpha$  and for  $\beta < \gamma$  let the program  $P_{\text{truth}}$  run on input  $((\xi, \beta, \vec{a}), \varphi^*)$  for  $\gamma$  steps. If the truth program stops with output 1, then stop, otherwise continue.

For the second part, let  $A \subseteq \alpha$  be  $\Delta_1(L_{\alpha}[B])$ . Then A and  $\alpha \setminus A$  are  $\alpha$ -computably enumerable in B, and hence A is  $\alpha$ -computable in B.  $\Box$ 

The results so far yield the following characterizations:

**Theorem 7.** Let the ordinal  $\alpha$  be closed under ordinal exponentiation and  $A \subseteq \alpha$ . Then

(a) A is  $\alpha$ -computable in B iff A is  $\Delta_1(L_{\alpha}[B])$ .

(b) A is  $\alpha$ -computably enumerable in B iff A is  $\Sigma_1(L_{\alpha}[B])$ .

#### 3. Admissible recursion theory

Admissible ordinals were defined by Kripke [7] in order to generalize standard recursion theory to ordinals. One of the many equivalent definitions is:

**Definition 8.** An ordinal  $\alpha \geq \omega$  is *admissible* iff there is no total  $\Sigma_1(L_\alpha)$ -definable function f that maps an ordinal  $\beta < \alpha$  cofinally into  $\alpha$ .

Note that every admissible ordinal is closed under ordinal exponentiation. By Theorem 7, we characterize admissibility in terms of ordinal computability without any reference to the constructible hierarchy.

**Theorem 9.** An ordinal  $\alpha$  closed under ordinal exponentiation is admissible iff there is no  $\alpha$ -computable function g that maps some  $\beta < \alpha$  cofinally into  $\alpha$ .

In case of an admissible ordinal  $\alpha$ , the absence of computable functions cofinal in  $\alpha$  enables us to make free use of nested loops of lengths  $< \alpha$  and finitely many working tapes with independent heads when describing algorithms for  $\alpha$ -machines. Recall the fundamental definitions of admissible recursion theory (see [11, p. 154–155]):

#### **Definition 10.** Let $\alpha$ be admissible. Then

- (a) x is  $\alpha$ -finite iff  $x \in L_{\alpha}$ .
- (b)  $A \subseteq \alpha$  is  $\alpha$ -recursive iff A is  $\Delta_1(L_\alpha)$ .

(c)  $A \subseteq \alpha$  is  $\alpha$ -recursively enumerable ( $\alpha$ -r.e.) iff A is  $\Sigma_1(L_{\alpha})$ .

Theorem 7 immediately gives the equivalences:

**Theorem 11.** Let  $\alpha$  be admissible. Then

- (a)  $A \subseteq \alpha$  is  $\alpha$ -recursive iff A is  $\alpha$ -computable.
- (b)  $A \subseteq \alpha$  is  $\alpha$ -r.e. iff A is  $\alpha$ -computably enumerable.

The notion of  $\alpha$ -finiteness can be characterized by means of  $\alpha$ -computability in the following way:

**Theorem 12.** Let  $\alpha$  be admissible. For  $A \subseteq \alpha$  it is equivalent:

- (a) A is  $\alpha$ -finite,
- (b) A is  $\alpha$ -computable and bounded below  $\alpha$ ,
- (c) A is  $\beta$ -computable for some  $\beta < \alpha$ .

**Proof.** (a)  $\Rightarrow$  (b) since  $\alpha$ -finite implies  $\alpha$ -computable and  $\alpha$  is a limit ordinal.

(b)  $\Rightarrow$  (c). Let  $A \subseteq \gamma < \alpha$  and let  $\{\epsilon\} = \chi_A$ . Since  $\alpha$  is admissible there exists an upper bound  $\delta < \alpha$  on the length of the computations  $\{\epsilon\}(\xi)$  for  $\xi < \gamma$ . So A is max $\{\delta, \gamma\}$ -computable.

(c)  $\Rightarrow$  (a). We may assume that  $\beta$  is a limit ordinal. By Lemma 3 *A* has a  $\Delta_1$ -definition over  $L_{\beta}$ . So  $A \in L_{\beta+1} \subseteq L_{\alpha}$ .  $\Box$ 

Identifying a function *f* with its graph { $\langle \zeta, \eta \rangle | f(\zeta) = \eta$ } (where  $\langle \cdot, \cdot \rangle$  is the Gödel-pairing function), we have a natural notion of  $\alpha$ -finite functions on ordinals. Also, the cardinals within  $L_{\alpha}$  can now be characterized by  $\alpha$ -machines.

**Theorem 13.** Let  $\alpha$  be admissible and  $\beta < \alpha$ . Then

- (a)  $L_{\alpha} \models \beta$  is a cardinal' iff there is no  $\delta < \beta$  and no  $\alpha$ -finite  $g : \delta \xrightarrow{surj} \beta$ . We call  $\beta$  an  $\alpha$ -cardinal.
- (b)  $L_{\alpha} \models \beta$  is a regular cardinal' iff  $\beta$  is an  $\alpha$ -cardinal and there is no  $\gamma < \beta$  with an  $\alpha$ -finite  $h : \gamma \xrightarrow{cof} \beta$ . In this case we say  $\beta$  is an  $\alpha$ -regular cardinal.

**Definition 14.** For  $\alpha$ -finite  $A \subseteq \alpha$  we denote by  $|A|_{\alpha}$  the  $\alpha$ -cardinality of A, i.e. the least ordinal  $\delta < \alpha$  with an  $\alpha$ -finite  $g : \delta \xrightarrow{surj} A$ .

Admissible recursion theory uses methods from Jensen's fine structure of the constructible hierarchy [4] as a partial substitute for the strong closure properties of  $\omega$ . The central notion of fine structure theory is given by the *projectum*. The following definition taken from [2] is equivalent to the original one.

**Definition 15.** For every ordinal  $\alpha \ge \omega$  define its *projectum* 

 $\alpha^* = \min\{\rho \leq \alpha \mid \exists g \ (g \ \Sigma_1(L_\alpha) \land g : L_\rho \xrightarrow{surj} L_\alpha)\}.$ 

Theorem 7 yields a characterization without reference to the constructible hierarchy.

**Theorem 16.** Let  $\alpha$  be admissible. Then  $\alpha^*$  is the smallest ordinal such that there is an  $\alpha$ -computable injection from  $\alpha$  into  $\alpha^*$ .

Lemma 17.

$$\alpha^* = \min\{\rho \leq \alpha \mid \exists \tilde{g} \; (\tilde{g} \; \boldsymbol{\Sigma}_1(L_\alpha) \land \tilde{g} : \rho \stackrel{\text{surg}}{\longrightarrow} \alpha)\}.$$

**Proof of Lemma** 17. Let g be  $\Sigma_1(L_{\alpha})$  with  $g : L_{\rho} \xrightarrow{surj} L_{\alpha}$ . There exists a  $\Delta_1(L_{\alpha}) h : \alpha \xrightarrow{bij} L_{\alpha}$  [11, Proposition 1.8, p. 156]. Similarly, we can obtain a  $\Delta_1(L_{\alpha})$ -definable  $\tilde{h} : \rho \xrightarrow{bij} L_{\rho}$ . Define  $\tilde{g} = h^{-1} \circ g \circ \tilde{h}$ . This is  $\Sigma_1(L_{\alpha})$  and  $\tilde{g} : \rho \xrightarrow{surj} \alpha$ . Conversely, let  $\tilde{g}$  be given as  $\Sigma_1(L_{\alpha})$  and with  $\tilde{g} : \rho \xrightarrow{surj} \alpha$ . Then  $g = h \circ \tilde{g} \circ \tilde{h}^{-1}$  is  $\Sigma_1(L_{\alpha})$  and  $g : L_{\rho} \xrightarrow{surj} L_{\alpha}$ .  $\Box$ 

**Proof of Theorem 16.** Since  $\alpha$  is admissible assume that we have a partial  $\alpha$ -computable  $g : \rho \xrightarrow{surj} \alpha$ . We define the  $\alpha$ computable  $f : \alpha \xrightarrow{inj} \rho$  as follows: Given input  $\xi$ , we check in stage  $\sigma$  the first  $\sigma$ -many steps of the computations of  $g(\iota)$ for  $\iota < \sigma$ . At some least stage we will find a least  $\eta$  with  $g(\eta) = \xi$  and set  $f(\xi) = \eta$ . If on the other hand we assume a  $f : \alpha \xrightarrow{inj} \beta$ , it suffices to define a partial  $\alpha$ -computable  $g : \beta \xrightarrow{surj} \alpha$ : Set  $g(\xi)$  to the  $\eta$  with  $f(\eta) = \xi$  and undefined else.  $\Box$ 

Admissible recursion theory studies subsets of  $\alpha$  with respect to certain reducibility relations. We define three reducibility notions. The first two are standard in  $\alpha$ -recursion theory (see [11, p. 162]), the third is the natural notion arising from  $\alpha$ -computability. Fix an admissible ordinal  $\alpha$  for the rest of this section.

**Definition 18.** (a) *A* is weakly  $\alpha$ -recursive in *B*,  $A \leq_{w\alpha} B$ , iff there exists an  $\alpha$ -recursively enumerable set  $R \subseteq L_{\alpha}$  such that for all  $\gamma < \alpha$ 

$$\gamma \in A \text{ iff } \exists H \subseteq B \exists J \subseteq \alpha \setminus B : (H, J, \gamma, 1) \in R$$

and

$$\gamma \notin A \text{ iff } \exists H \subseteq B \exists J \subseteq \alpha \setminus B : (H, J, \gamma, 0) \in R.$$

(b) A is  $\alpha$ -recursive in B,  $A \leq_{\alpha} B$ , iff there exist  $\alpha$ -r.e. sets  $R_0, R_1 \subseteq L_{\alpha}$  such that for all  $K \in L_{\alpha}$ 

$$K \subseteq A \text{ iff } \exists H \subseteq B \exists J \subseteq \alpha \setminus B : (H, J, K) \in R_0$$

and

$$K \subseteq \alpha \setminus A \text{ iff } \exists H \subseteq B \exists J \subseteq \alpha \setminus B : (H, J, K) \in R_1.$$

**Definition 19.** For subsets A,  $B \subseteq \alpha$  define  $A \preceq_{\alpha} B$ , A is  $\alpha$ -computably reducible to B, iff A is  $\alpha$ -computable in the oracle B.

By Theorem 7 this can be reformulated as:

**Theorem 20.** For  $A, B \subseteq \alpha$ :  $A \preceq_{\alpha} B$  iff  $A \in \Delta_1(L_{\alpha}[B])$ .

It is easy to see that  $A \leq_{\alpha} B$  implies  $A \leq_{w\alpha} B$ . The relation  $\leq_{\alpha}$  is transitive, whereas  $\leq_{w\alpha}$  and  $\leq_{\alpha}$  may fail to be transitive (see [3]). Inspection of the definition of  $\leq_{w\alpha}$  shows:

**Theorem 21.** If A is weakly  $\alpha$ -recursive in B then A is  $\alpha$ -computable in B.

Thus we have the following inclusions

 $\leq_{\alpha} \subseteq \leq_{w\alpha} \subseteq \preceq_{\alpha}$ .

The inclusions can in general not be reversed.

#### 4. The Sacks-Simpson theorem with machines

The first major result in  $\alpha$ -recursion theory was Sacks' and Simpson's proof of a positive answer to Post's problem for  $\alpha$ -recursion:

**Theorem 22** (Sacks–Simpson 1972). Let  $\alpha$  be an admissible ordinal. Then there are two  $\alpha$ -r.e. sets  $A \subseteq \alpha$  and  $B \subseteq \alpha$  such that  $A \not\leq_{w\alpha} B$  and  $B \not\leq_{w\alpha} A$ .

Post's problem is usually seen as a test case for recursion theories. So we are interested whether or not some priority argument can be implemented on our  $\alpha$ -machines. More precisely, can we utilize the lim inf-rule in a way that the algorithm behaves correctly at limit times, preserving all the necessary information? We will see that it is instrumental to store information in different data structures (see (1) and (2) below).

We base our considerations on the presentations of the Sacks–Simpson theorem in [11,12]. There, the proof is divided in two cases, depending on the closure properties of the admissible ordinal  $\alpha$ . Each case is proved by describing an algorithm that simultaneously enumerates the two sets *A* and *B* as required.

The first case,  $\alpha^* < \alpha$ , uses a generalization of the classical Friedberg–Muchnik algorithm, but with requirements indexed by  $\alpha^*$ . In the correctness proof, an analysis of the  $\alpha$ -cardinality structure ensures that all injury sets are  $\alpha$ -finite. Instead of employing the complicated method of tame  $\Sigma_2$ -maps as in [11,12], an adaptation of the method described by Shore in [13] can be used to handle the case that  $\alpha^* = \alpha$ , as briefly suggested in the non-trivial Exercise VIII.1.5 in [11]: The above algorithm is modified by adding an additional set of negative requirements which keep the injury sets literally finite. The closure of  $\alpha$  under partial  $\Sigma_1$ -maps ensures the correctness despite the heavily restricted injury behavior.

The techniques to implement either of these algorithms on  $\alpha$ -machines are the same, therefore we choose to restrict our presentation to the algorithm for the case  $\alpha^* < \alpha$ .

From now on we assume  $\alpha^* < \alpha$ .

By Theorem 21, the Sacks–Simpson theorem can be proved by giving an algorithm that computably enumerates two sets *A* and *B*, for which the goals  $A \not\preceq_{\alpha} B$  and  $B \not\preceq_{\alpha} A$  hold. Consider the following requirements:

$$\operatorname{req}_{2\epsilon}$$
: If  $\epsilon \in \operatorname{ran}(f)$  then  $\{f^{-1}(\epsilon)\}^B \neq \chi_A$ 

 $\operatorname{req}_{2\epsilon+1}$ : If  $\epsilon \in \operatorname{ran}(f)$  then  $\{f^{-1}(\epsilon)\}^A \neq \chi_B$ 

with a fixed map  $f : \alpha \xrightarrow{inj} \alpha^*$  according to Theorem 16.

Along with the sets A and B the algorithm will give a witness for every  $req_{2\epsilon}$  with  $\epsilon \in ran(f)$ , i.e., an ordinal  $w_{2\epsilon}$  for which

$${f^{-1}(\epsilon)}^{B}(w_{2\epsilon}) \downarrow = 0 \text{ iff } w_{2\epsilon} \in A$$

and likewise a witness  $w_{2\epsilon+1}$  for req<sub>2 $\epsilon+1$ </sub>.

At any given stage  $\sigma < \alpha$  of the algorithm we have the following data for every requirement req<sub>2</sub> $\epsilon$  (making use of the  $\alpha$ -computable Gödel-pairing function  $\langle \cdot, \cdot \rangle$  for coding):

- (1)  $w_{2\epsilon}^{\sigma}$  is the candidate for the witness for req<sub>2e</sub> at stage  $\sigma$ . Those candidate witnesses only *increase* over time, so we can represent them on one tape in a way that at any given time  $\sigma$  the tape contains a 1 in cell  $\langle 2\epsilon, \gamma \rangle$  iff  $\gamma < w_{2\epsilon}^{\sigma}$ . Instead of the single ordinal  $w_{2\epsilon}^{\sigma}$ , the complete initial segment of ordinals up to  $w_{2\epsilon}^{\sigma}$  is coded onto the tape.
- (2)  $\operatorname{used}_{2\epsilon}^{\sigma}$  is non-zero iff  $w_{2\epsilon}^{\sigma} \in A$  and is set to the supremum over the cell indices used in a the computation responsible for putting  $w_{2\epsilon}^{\sigma}$  into A or B. Those are stored on one tape whose  $\langle 2\epsilon, \beta \rangle$  th cell contains a 1 iff  $\operatorname{used}_{2\epsilon}^{\sigma} = \beta$ . We want  $\operatorname{used}_{2\epsilon}^{\sigma} = 0$  to code the information that  $w_{2\epsilon}^{\sigma} \notin A$ . The variable  $\operatorname{used}_{2\epsilon}^{\sigma}$  is represented as a single ordinal bit on the tape.

All of these are analogously defined for  $\operatorname{req}_{2\epsilon+1}$  with the roles of *A* and *B* interchanged. *A* and *B* are constructed as their characteristic functions on one tape each,  $A^{\sigma}$  resp.  $B^{\sigma}$  denote the part constructed at the beginning of stage  $\sigma$ . The candidate witnesses  $w_{\delta}^{\sigma}$  for  $\operatorname{req}_{\delta}$  are chosen from the class  $Z_{\delta} = \{\langle \delta, \xi \rangle \mid \xi \in \operatorname{Ord}\}$  so  $w_{\delta}^{\sigma}$  can only enter *A* or *B* as a witness for  $\operatorname{req}_{\delta}$ .

In what follows, whenever a statement is made about  $\operatorname{req}_{2\epsilon}$ , its dual for  $\operatorname{req}_{2\epsilon+1}$  is also assumed yet omitted for simplicity. We say that  $\operatorname{req}_{2\epsilon}$  is *not currently witnessed* at stage  $\sigma$  if  $w_{2\epsilon}^{\sigma} \notin A^{\sigma}$ .  $\operatorname{req}_{2\epsilon}$  acts at stage  $\sigma$  when  $w_{2\epsilon}^{\sigma}$  is added to A at stage  $\sigma$ . As long as  $w_{2\epsilon}^{\sigma} \in A^{\sigma}$  we call  $\operatorname{req}_{2\epsilon}$  currently witnessed.  $\operatorname{req}_{2\epsilon}$  is injured by  $\operatorname{req}_{\delta}$  in stage  $\sigma$  if  $w_{2\epsilon}^{\sigma} \in A^{\sigma}$  but  $w_{2\epsilon}^{\sigma+1} \notin A^{\sigma+1}$  and  $\operatorname{req}_{\delta}$  is the requirement acting in stage  $\sigma$ .

The following Friedberg–Muchnik-type algorithm will check each requirement unboundedly often, but every requirement will act only  $\alpha$ -finitely many times [12].

**Algorithm 1.** The algorithm goes through  $\alpha$ -many stages  $\sigma$  and in each stage it might put an element into *A* or *B* to fulfill one requirement (if so, we say that the respective requirement acts).

 $\sigma = 0$ : Initialize  $w_0^0 = \langle 0, 0 \rangle$  and used $_0^0 = 0$ .

 $\sigma \mapsto \sigma + 1$ : Let  $\sigma = \mu \cdot \alpha^* + \nu$  with  $\nu < \alpha^*$ . If  $w_{\nu}^{\sigma}$  does not have a value, initialize  $w_{\nu}^{\sigma} = \min\{\xi \in Z_{\nu} \mid \xi \notin A^{\sigma} \cup B^{\sigma} \land \xi > \sup\{\operatorname{used}_{\nu}^{\sigma} \mid \gamma < \nu\}$  and  $\operatorname{used}_{\nu}^{\sigma} = 0$ . [This initialization happens exactly at stages  $< \alpha^*$ .] Let WLOG  $\nu = 2\epsilon$ .

If  $w_{2\epsilon}^{\sigma} \in A^{\sigma}$  then  $\operatorname{req}_{2\epsilon}$  is currently witnessed and no action is taken, i.e., set  $A^{\sigma+1} = A^{\sigma}$ ,  $B^{\sigma+1} = B^{\sigma}$ ,  $w_{\delta}^{\sigma+1} = w_{\delta}^{\sigma}$  and  $\operatorname{used}_{\lambda}^{\sigma+1} = \operatorname{used}_{\lambda}^{\sigma}$  for all  $\delta < \alpha^*$ .

If  $w_{2\epsilon}^{\sigma} \notin A$  then check whether  $\{f^{-1}(\epsilon)\}_{\sigma}^{B^{\sigma}}(w_{2\epsilon}^{\sigma}) \downarrow = 0.$ 

If this is not true then again no action is taken.

If  $\{f^{-1}(\epsilon)\}^{B^{\sigma}}_{\sigma}(w^{\sigma}_{2\epsilon}) \downarrow = 0$  then  $\operatorname{req}_{2\epsilon}$  acts: set  $A^{\sigma+1} = A^{\sigma} \cup \{w^{\sigma}_{2\epsilon}\}, B^{\sigma+1} = B^{\sigma}, w^{\sigma+1}_{2\epsilon} = w^{\sigma}_{2\epsilon}$ . Let u be the supremum of the indices of cells used in the computation of  $\{f^{-1}(\epsilon)\}^{B^{\sigma}}_{\sigma}(w^{\sigma}_{2\epsilon}) \downarrow = 0$  and set  $\operatorname{used}^{\sigma+1}_{2\epsilon} = u$ . For  $\delta < 2\epsilon$  keep  $w^{\sigma+1}_{\delta} = w^{\sigma}_{\delta}$ ,  $\operatorname{used}^{\sigma+1}_{\delta} = \operatorname{used}^{\sigma}_{\delta}$ . For  $\alpha^* > \delta > 2\epsilon$  and  $\delta \le \sigma$ :  $\operatorname{used}^{\sigma+1}_{\delta} = 0, w^{\sigma+1}_{\delta} = \min\{\xi \in Z_{\delta} \mid \xi \notin A^{\sigma+1} \cup B^{\sigma+1} \land \xi > w^{\sigma}_{\delta} \land \xi > u\}$ .

Note that the description of the algorithm does not contain a limit step since the machine configuration at limit times is completely determined by the lim inf-rules.

We need to check whether the variables contain their intended values also at limit stages:

**Lemma 23.** At the beginning of every stage  $\sigma$  the following conditions hold for every  $\delta < \alpha^*$  resp.  $2\epsilon < \alpha^*$ :

(a)  $w_{\delta}^{\sigma} > \sup\{\operatorname{used}_{\gamma}^{\sigma} \mid \gamma < \delta\},\$ 

(b) If  $w_{2\epsilon}^{\sigma} \in A$  then  $\{f^{-1}(\epsilon)\}_{\sigma}^{B^{\sigma}}(w_{2\epsilon}^{\sigma}) \downarrow = 0$  and used  $\frac{\sigma}{2\epsilon}$  is the supremum over the indices of the cells used in this computation.

**Proof.** If  $\sigma$  is a successor ordinal, the lemma holds by the definition of w and used. If  $\sigma$  is a limit consider a requirement req<sub>2</sub> $\epsilon$ . If req<sub>2</sub> $\epsilon$  acted at a stage  $\tau < \sigma$  and no requirement req<sub>3</sub> with  $\delta < 2\epsilon$  acted at a stage  $\rho, \tau < \rho < \sigma$ , then  $w_{2\epsilon}^{\tau} = w_{2\epsilon}^{\sigma}$  and (a) holds. In fact  $\{f^{-1}(\epsilon)\}_{\sigma}^{B^{\sigma}}(w_{2\epsilon}^{\sigma}) \downarrow = 0$  is the same computation as  $\{f^{-1}(\epsilon)\}_{\tau}^{B^{\tau}}(w_{2\epsilon}^{\tau}) \downarrow = 0$  since the elements in  $B^{\sigma} \setminus B^{\tau}$  are all larger than the supremum of cell indices used in  $\{f^{-1}(\epsilon)\}_{\tau}^{B^{\tau}}(w_{2\epsilon}^{\tau}) \downarrow = 0$ . Since used  $f_{2\epsilon}^{\tau} = used_{2\epsilon}^{\sigma}$  also (b) holds. If, on the other hand, req<sub>2</sub> $\epsilon$  was injured unboundedly often in  $\sigma$ , then, by the liminf-rule, used  $f_{2\epsilon}^{\sigma} = 0$  and  $w_{2\epsilon}$  has been the formula of the product of the supremum of the product of the produ

If, on the other hand,  $\operatorname{req}_{2\epsilon}$  was injured unboundedly often in  $\sigma$ , then, by the lim inf-rule,  $\operatorname{used}_{2\epsilon}^{\sigma} = 0$  and  $w_{2\epsilon}$  has been redefined unboundedly often in  $\sigma$ . Since, unlike  $\operatorname{used}_{2\epsilon}, w_{2\epsilon}$  is represented as an initial segment of '1's on the tape, it does not default to zero but takes its intended value at stage  $\sigma$ . By admissibility  $w_{2\epsilon}^{\sigma}$  is well defined, i.e.  $< \alpha$ . So (a) holds inductively. Since  $w_{2\epsilon}^{\sigma} \notin A$ , (b) is trivially true in this case.  $\Box$ 

Seeing that the data structures behave well at limit times, it is clear that this algorithm is similar to the ones from [12] and [11] and therefore correctly solves Post's problem for  $\alpha$ -recursion theory in case that  $\alpha^* < \alpha$ .

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